

TABLE B: Main Inputs /stages for manufacture of telecom products & conditions for the inputs to be qualified as Local Content

Main Inputs /stages for manufacture of telecom products*	Conditions for the inputs to be qualified as Local Content
(1) Design (a) Hardware design (b) Software Design &Development	The maximum Local Content (LC) percentage for Design which can be claimed by a Local manufacturer for the telecom products based on in-house / in country R&D costs incurred/amortized to create IPR in India are as per Table-C subject to the condition that: (a) The Intellectual Property Right (IPR) resides in India for Hardware Design, (b) The Copyright is in India for the software Design & Development.
(2) Components (a) Integrated chips (ICs) – Processor, Memory, etc. (b) Active components – Transistors, Diodes etc. (c) Passive Components – Resistors, Capacitor, Inductors etc.	Manufactured in India
(3) PCBs (a) PCB Fabrication (b) PCB population using components	Manufactured in India
(4) Cables/Chassis etc. (a) Chassis (b) Cables (c) Racks (d) Heat sinks (e) Enclosures	Manufactured in India
(5) RF Components/Subsystem (a) Duplexers/Filters (b) Antenna	Manufactured in India
(6) Assembly/Integration/Testing#	The upper ceiling limit of Domestic Local Content (LC) for Assembly/ Integration/ Testing in respect of the telecom products listed in Table-C would be 10% of the total product Bill of Material (except S. No. 25,26 and 36)

*The product may include some/all of the input/stage as mentioned above. While calculating only those inputs/stages will be calculated which are involved in the manufacturing of these telecom products.
In case a system of its subsystem is merely assembled / integrated / tested, then actual Local Content shall be taken as up to 10% only of the cost of system / subsystem.